Serie 3 - Safety and Synchronization

Exercise 1

Answer the following questions:

1. What are safety properties? How are they modeled in FSP?
2. Is the busy-wait mutex protocol fair? Deadlock-free? Justify your answer.
3. Can you ensure safety in concurrent programs without using locks?
4. The Java language designers decided to implement concurrency based on monitors. What is the main reason behind this decision?

Exercise 2

Consider the following process definitions:

\[
\begin{align*}
P &= (a\rightarrow b\rightarrow P), \\
Q &= (c\rightarrow b\rightarrow Q), \\
||S1 &= (P | | Q), \\
S2 &= (a\rightarrow c\rightarrow b\rightarrow S2 | c\rightarrow a\rightarrow b\rightarrow S2).
\end{align*}
\]

Are S1 and S2 equivalent? Why?

Exercise 3

A miniature portable FM radio has three controls. An on/off switch turns the device on and off. Tuning is controlled by two buttons scan and reset which operate as follows. When the radio is turned on or reset is pressed, the radio is tuned to the top frequency of the FM band. When scan is pressed, the radio scans towards the bottom of the band. It stops scanning when it locks on to a station or it reaches bottom (end). If the radio is currently tuned to a station and scan is pressed then it starts to scan from the frequency of that station towards bottom. Similarly, when reset is pressed the receiver tunes to top.

Using the alphabet \{on, off, scan, reset, lock, end\} model the FM radio as an FSP process called RADIO and generate the corresponding LTS using the LTSA tool.

Exercise 4

Consider the following problem specifications:

1. We have a stack that has two slots.
2. The stack operations \textit{pop} and \textit{push} should be atomic. In other words, the stack is locked while being popped or pushed.
3. The input space is 0..1 (You can only \textit{push} 0s and 1s).
4. There is one producer process that *pushes* 0s and 1s repeatedly.

5. There is one consumer that *pops* the top of the stack.

6. Throw an error for *pushing* to a full stack or *popping* from an empty stack.

Write an FSP description for this problem and verify your model using LTSA.